

Representative Vell Placement



PETER C. WALTERS SVP OF ENGINEERING

HILLSBORO, OREGON - Peter has extensive experience in business and product development for new businesses in test and measurement front-ends, automotive radar, electronic phase arrays, CMDA handsets PAs, front-end satellite communication systems, 10G physical layer transport telecom, and newEDA design and validation flows. Peter has helped raise over \$50M in funding and defined customer significant products leading to \$200M yearly revenues in multiple large and small company settings. Proven leader and team player through previously held positions including CEO, VP of Engineering, VP of Business Development, and various senior management and design positions within companies such as Multigig, iTerra, Wavetronix, Hewlett Packard, Rockwell, and Philips. He has extensive experience in High-Speed Electronic Integrated Circuits applications with multiple semiconductor technologies. Peter holds a number of patents, including the first on-wafer probe calibration structures to 75GHz. He was a research fellow at the European Space Agency and has a Ph.D. in Electrical Engineering from the University of Leeds, England, MBA, and B.A.Sc from the University of British Columbia, Canada.

About AISI

AISI is changing the face of semiconductor test. AISI's revolutionary WaferTranslator™ technology makes full wafer test and burn-in an economic reality. AISI's vision is to become the world's leading provider of Advanced Inquiry Systems for microelectronics. AISI's strategic mission is to rapidly bring to market a game changing world class solution for wafer level test using a high margin business model and innovative technologies. Funded in 2005 and headquartered in Hillsboro, Oregon, they deliver cost effective solutions to the multi-billion-dollar market for semiconductor test in the form of full wafer contactors, tester interfaces, and automated test platforms.